

S/N 09/347690

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manpreet S. Khaira et al.

Examiner: Dwin M Craig

Serial No.: 09/347690

Group Art Unit: 2123

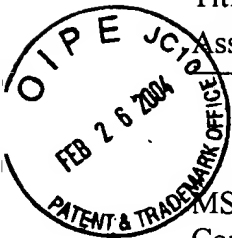
Filed: July 2, 1999

Docket No.: 884.107US1

Title: LOGIC VERIFICATION IN LARGE SYSTEMS

Assignee: Intel Corporation

Customer No: 21186



AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This responds to the Office Action mailed on December 22, 2003. Please amend the above-identified patent application as follows, and consider the appended remarks.

**RECEIVED**

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